

NPGL-HM11D

NPGL-HM111D

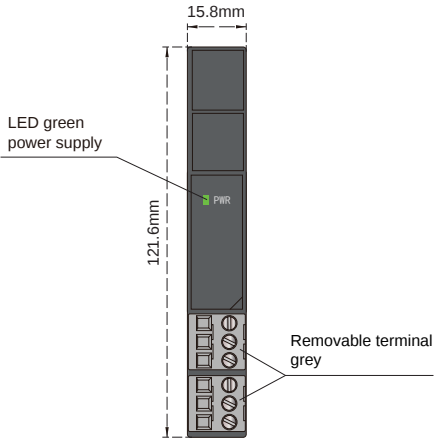
NPGL-HMD111D

Input: 4 ~ 20 mA

Output: 4 ~ 20 mA

single input, single output
single input, double output
double input, double output

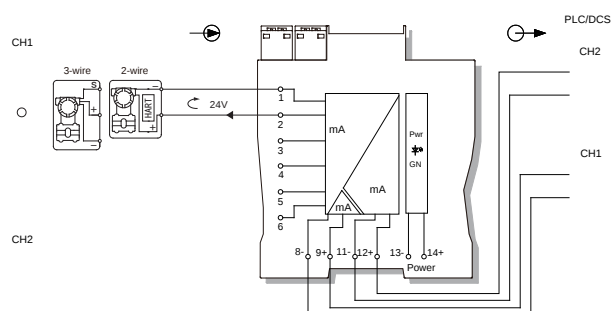
This isolator converts the current or voltage signals to current or voltage signals. It allows transmission of HART communication signals. The input, output, and power supply are galvanically isolated from each other.



Technical data	
Power supply:	18 V DC~32 V DC (Reverse power protection)
Power dissipation:	
Input signal:	Current: 0/4 ~ 20mA; 0 ~ 10mA Voltage: 0/1 ~ 5V; 0 ~ 10V
Input resistance:	Current: Voltage:
Available voltage:	
Output signal:	 ~ 20mA Current: 0/4 ~ 20mA; 0 ~ 10mA Voltage: 0/1 ~ 5V; 0 ~ 10V
Load resistance:	 RL supply 0/4 ~ 20mA: RL ~ 10mA: RL 0/1 ~ 5V: RL ~ 10V: RL
Accuracy:	± 0.1%F.S.
Temperature drift:	0.005%F.S./°C
Response time:	
Electromagnetic compatibility:	IEC 61326-3-1
Dielectric strength:	
Insulation resistance:	
Operation temperature:	-20°C ~ +60°C
Storage temperature:	-40°C ~ +80°C
Dimension:	15.8 mm (W) × 121.6 mm (H) × 104.8 mm (D)

Model rules						Description
NPWD-HM	☐	☐	☐	☐	☐	Isolator
Channel	☐	☐	☐	☐	☐	Single as default
	D	☐	☐	☐	☐	Double channel
Input	1	☐	☐	☐	☐	4~20mA
	2	☐	☐	☐	☐	1~5V
	3	☐	☐	☐	☐	0~10mA
	4	☐	☐	☐	☐	0~5V
	5	☐	☐	☐	☐	0~10V
	6	☐	☐	☐	☐	0~20mA
Output1	1	☐	☐	☐	☐	4~20mA
	1S	☐	☐	☐	☐	 ~20mA
	2	☐	☐	☐	☐	1~5V
	3	☐	☐	☐	☐	0~10mA
	4	☐	☐	☐	☐	0~5V
	5	☐	☐	☐	☐	0~10V
Output2	6	☐	☐	☐	☐	0~20mA
		☐	☐	☐	☐	None as default
	1	☐	☐	☐	☐	4~20mA
	1S	☐	☐	☐	☐	 ~20mA
	2	☐	☐	☐	☐	1~5V
	3	☐	☐	☐	☐	0~10mA
Power supply	4	☐	☐	☐	☐	0~5V
	5	☐	☐	☐	☐	0~10V
	6	☐	☐	☐	☐	0~20mA
	D	☐	☐	☐	☐	24V DC

Wiring diagram



24V DC, double current input, double source current output

24V DC, single voltage input, double current / voltage output